

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KARSTEN WIECZOREK
THOMAS FEUDEL
THORSTEN KAMMLER
WOLFGANG BUCHHOLTZ

Confirmation No.: 3105

Examiner: N. Drew Richards

Group Art Unit: 2815

Att'y Docket: 2000.110600/DE0347

Customer No.: 23720

Serial No.: 10/790,852

Filed: March 2, 2004

For: FIELD EFFECT TRANSISTOR HAVING A
DOPED GATE ELECTRODE WITH
REDUCED GATE DEPLETION AND
METHOD OF FORMING THE TRANSISTOR

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

With regard to the Statement of Reasons for Allowance, the Examiner made reference to various portions of the language of the pending claims. However, as the Examiner well knows, it is the entirety of the claimed invention that must be considered for purposes of determining patentability. Thus, the Examiner's reference to certain portions of the language of the pending claims should not be understood to somehow limit the presently claimed invention or to give special importance to the language identified by the Examiner. As set forth above, it is the entirety of each of the pending claims that must be considered for purposes of patentability.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON
CUSTOMER NO. 23720

Date: January 12, 2009

/J. Mike Amerson/

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